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APPLICATION NO.		ING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/848,263 05/04/2001		Naoki Furuhata	PF-2801/NEC/US/mh/B1(ELD) 6317		
466	7590	05/02/2003			
YOUNG &			EXAMINER		
745 SOUTH 23RD STREET 2ND FLOOR ARLINGTON, VA 22202			₹	COLEMAN, WILLIAM D	
				ART UNIT	PAPER NUMBER
				2823	
			DATE MAILED: 05/02/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)				
		09/848,263	FURUHATA, NAOKI				
٠.	Office Action Summary	Examiner	Art Unit				
		W. David Coleman	2823				
Period f	The MAILING DATE of this communication aporton reply	pears on the cover sheet with t	the correspondence address				
	HORTENED STATUTORY PERIOD FOR REPL	Y IS SET TO EXPIRE 3 MON	TH(S) FROM				
THE - Ext afte - If th - If N - Fail - Any	MAILING DATE OF THIS COMMUNICATION. ensions of time may be available under the provisions of 37 CFR 1. It SIX (6) MONTHS from the mailing date of this communication. He period for reply specified above is less than thirty (30) days, a reportion of the provision	136(a). In no event, however, may a reply ly within the statutory minimum of thirty (30 will apply and will expire SIX (6) MONTHS e, cause the application to become ABANI	be timely filed)) days will be considered timely. I from the mailing date of this communication. ONED (35 U.S.C. § 133).				
1)🛛	Responsive to communication(s) filed on 12	September 2002 .					
2a)⊠	<u> </u>	nis action is non-final.					
3)	closed in accordance with the practice under						
· ·	tion of Claims						
4)⊠	Claim(s) <u>1-18,42,45,47 and 53</u> is/are pending						
_	4a) Of the above claim(s) is/are withdrawn from consideration.						
· · · · · · · · · · · · · · · · · · ·	Claim(s) 42,45 and 47 is/are allowed.						
·	Claim(s) 1,2 and 53 is/are rejected.						
•	Claim(s) 3-18 is/are objected to.						
,	Claim(s) are subject to restriction and/o tion Papers	or election requirement.					
	The specification is objected to by the Examine	ar					
•	The drawing(s) filed on is/are: a)☐ acce		Evaminer				
10)	Applicant may not request that any objection to the						
11)	The proposed drawing correction filed on	_ is: a) approved b) disa					
٠٠,	If approved, corrected drawings are required in re		,				
12)	The oath or declaration is objected to by the Ex	• •					
Priority	under 35 U.S.C. §§ 119 and 120						
•	Acknowledgment is made of a claim for foreig	n priority under 35 U.S.C. § 1	19(a)-(d) or (f).				
•)⊠ All b)□ Some * c)□ None of:						
	1.⊠ Certified copies of the priority documen	ts have been received.					
	2. Certified copies of the priority documen	ts have been received in App	lication No				
*	3. Copies of the certified copies of the price application from the International Boundary See the attached detailed Office action for a list	ureau (PCT Rule 17.2(a)).					
14)	Acknowledgment is made of a claim for domes	tic priority under 35 U.S.C. § 1	19(e) (to a provisional application).				
	 a) The translation of the foreign language pr Acknowledgment is made of a claim for domes 						
Attachme	<u> </u>						
2) Not	ice of References Cited (PTO-892) ice of Draftsperson's Patent Drawing Review (PTO-948) ormation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Info	nmary (PTO-413) Paper No(s) rmal Patent Application (PTO-152)				
S Patent and	Trademark Office						

DETAILED ACTION

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1 and 2 are rejected under 35 U.S.C. 102(b) as being anticipated by Asano, Japanese Patent Abstracts 10-107042.

pertaining to claim 1, <u>Asano</u> discloses a semiconductor device as claimed. See Drawing 1, where <u>Asano</u> discloses a monolithically integrated semiconductor device comprising:

a hetero-junction bipolar transistor having at least one electrode contact layer 20 which contacts directly with at least one of collector, base and emitter electrodes 22; and

at least one passive device 31 having at least one passive device electrode 24 having two resistive elemental electrode contacts and at least one resistive element layer 20, contacting said two resistive element electrode contacts,

wherein said electrode contact layer and said restive element layer comprise the same compound semiconductor layer (i.e., GaInP, gallium indium phosphide which is a compound semiconductor layer).

3. Pertaining to claim 2, Asano teaches the device as claimed in claim 1, wherein said passive device electrode and one of said collector, base and emitter electrodes comprises the same metal layer (i.e., AuGe/Ni/Au).

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Claim Rejections - 35 USC § 103

4. Claim 53 is rejected under 35 U.S.C. 103(a) as being unpatentable over Asano, Japanese Patent Abstracts 10-107042 in view of Prasad et al., U.S. Patent 5,268,315.

5. <u>Asano</u> discloses a semiconductor device substantially as claimed. Asano teaches a monolithic integrated semiconductor device comprising:

a hetero-junction bipolar transistor having at least one electrode contact layer that directly contacts with at leas one of collector, base and emitter electrodes;

at least one passive device having at least one passive device electrode and at least one resistive layer and said electrode contact layer and said resistive layer are the same material.

However, <u>Asano</u> fails to teach a plurality of first level interconnects directly contacting each of said collector, base, emitter and device electrodes; and

a plurality of coplanar second level interconnects, perpendicular to and directly contacting said plural first level interconnects. Prasad teaches a plurality of first level interconnects directly contacting each of said collector, base, emitter and device electrodes; and

a plurality of coplanar second level interconnects, perpendicular to and directly contacting said plural first level interconnects. See **FIG. 15** (elements **86, 100** and **102**) where Prasad teaches the claimed limitation. In view of Prasad, it would have been obvious to one of ordinary skill in the art to incorporate the limitations of Prasad into the Asano device because it provides an air-bridge metal (column 7, lines 14-20).

Objections

Claims 3-18 are objected to as being dependent upon a rejected base claim, but would be 6. allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Allowable Subject Matter

- 7. Claims 42, 45 and 47 allowed.
- The following is an examiner's statement of reasons for allowance: reasons for allowance 8. are recited in Applicants arguments filed September 12, 2002 in paper no. 7.
- Any comments considered necessary by applicant must be submitted no later than the 9. payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

10. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, THIS ACTION IS MADE FINAL. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

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however, will the statutory period for reply expire later than SIX MONTHS from the date of this

final action.

11. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to W. David Coleman whose telephone number is 703-305-0004.

The examiner can normally be reached on 9:00 AM-5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Olik Chaudhuri can be reached on 703-306-2794. The fax phone numbers for the

organization where this application or proceeding is assigned are 703-308-7722 for regular

communications and 703-308-7721 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is 703-308-0956.

W. David Coleman

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Primary Examiner

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WDC

April 30, 2003